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For: COMPUTER NETWORK HAVING COMMONLY
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**COMPUTER NETWORK
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**COMPUTER NETWORK
HAVING COMMONLY LOCATED COMPUTING SYSTEMS**

Cross-Reference to Related Provisional Application

This application claims the benefit of U.S. Provisional Patent Application Serial No. 60/071,604, filed on January 16, 1998.

Cross-Reference to Related Applications

This application is related to U.S. Patent Application Serial No. 09/_____ (Atty. Docket No. 10393.42) entitled COMPUTER SYSTEM HAVING REMOTELY LOCATED I/O DEVICES, 09/_____ (Atty. Docket No. 10393.47) entitled METHOD FOR INCORPORATING COMPUTER DATA INTO AN ANALOG VIDEO STREAM AND AN ASSOCIATED COMPUTER SYSTEM HAVING REMOTELY LOCATED I/O DEVICES and 09/_____ (Atty. Docket No. 10393.61) entitled COMPUTER NETWORK HAVING MULTIPLE REMOTELY LOCATED HUMAN INTERFACES SHARING A COMMON COMPUTING SYSTEM, all of which were filed on even date herewith, assigned to the Assignee of the present application and are hereby incorporated by reference as if reproduced in their entirety.

Technical Field

The invention relates generally to computer networks and, more particularly, to a computer network which includes plural commonly located computing systems as a portion thereof.

Background of the Invention

In its broadest sense, a computer network is a set of nodes and communication channels which interconnect the set of nodes. The nodes may be computers, terminals, workstations, or communication units of various kinds and may be distributed at different locations. They communicate over the communication channels which are provided by the owner of the computer network or leased from a common carrier. These communication channels may use a variety of transmission media such as optical fibers, coaxial cable or twisted copper pairs. A local area network (or "LAN") is a computer network at a single site and, in many cases, is confined to a single building. A wide area network (or "WAN") is a computer network that uses either a public or private switching system to interconnect computers located at plural sites which may be separated by hundreds or thousands of miles.

There are a number of advantages to constructing a computer network. They include resource and data sharing, and communication and data exchange. Resource sharing provides users with convenient access to special computing resources, regardless of their physical location. Data sharing provides users with access to common databases. Data exchanges enable users to exchange data files while communication exchanges enable users to exchange messages, for example, via electronic mail (or "E-mail"). While networks may be arranged in a variety of configurations, a commonly used network design has a bus (also known as a "linear") topology in which a single network cable is routed through those locations where a data

terminal equipment (or "DTE") device is to be connected to the network. At each of these locations, a physical connection (or "tap") is made to the cable to allow the DTE at that location to access the network. At selected nodes of such a network, file servers or other large scale computer systems provide network services while, at others of the nodes, individual workstations, each typically comprised of a personal computer (or "PC"), desktop computer, or other type of physically compact computer system capable of both operating as a standalone computer and accessing the network services, reside.

The components of PCs (as well as all other computer systems, including minicomputers and mainframes), may be divided into two functional units--the computing system and the human interface (or "HI") to the computing system. For a PC, the computing system is, quite simply, the chassis which holds the motherboard, power supply, hard drive and the like. The human interface, on the other hand, are those devices that humans use to transfer information to and/or receive information from the computing system. The most commonly recognized devices which form part of the human interface with the computing system include the monitor, keyboard, mouse and printer. Of course, a variety of other devices, for example, a joystick, trackball, touchpad or others too numerous to specifically mention, may form part of the human interface. For most PCs installed at workstations, the computer monitor, keyboard and mouse rest on the desktop while the computer chassis which holds the computing system rests on the floor underneath the desktop.

While the above-described network configuration is quite common in many business establishments, recently, a number of issues, in particular, security concerns, have been raised in connection with such network designs. Business contacts, vendor information, contracts, reports, compilations, proprietary software, access codes, protocols, correspondence, account records, business plans are just some of the fundamental assets of a

company which are oftentimes accessible from an employee's computer where it can be quickly copied onto a floppy disk and stolen.

Disk and CD drives may also be used to introduce illegal, inappropriate or dangerous software to a computer. Storing bootlegged software can expose a company to copyright infringement claims. Computer games often reduce employee productivity. If imported onto a computer system, computer pornography may create a hostile work environment which leads to a sexual discrimination lawsuit against the company. Computer viruses can cause the loss of critical information stored on a computer. Finally, the computing system itself may be damaged or otherwise misconfigured when left accessible to technically oriented employees who take it upon themselves to attempt to repair and/or modify the computer system.

Another concern often raised in connection with the present practice of placing the computer system at the desktop is that such workstation designs actual work against proper maintenance of the computing system. When placed underneath the desktop, computing systems are often forced to absorb physical shocks when accidentally kicked, knocked over or struck by falling objects, any of which could result in damage to the various electronic components, located within the chassis, which comprises the computing system. Oftentimes, a computing system is placed in a "convenient" location and not in a location designed to keep it cool. A computer system typically includes a cyclonic fan designed to direct a constant flow of cooling area at the heat-generating components of the computing system. However, if a barrier is placed a few inches in front of the fan intake, the efficiency of the fan is reduced dramatically. Similarly, placing the computer system against a wall or running cables in front of the fan adversely affects the ability of the fan to properly cool the computing system. Finally, even in relatively clean office environments, the fan tends to draw in dirt and other dust

particles into the interior of the computer chassis where they are deposited on the heat-generating electronic components which comprise the computing system. As dust tends to insulate the components on which it is deposited, the ability of such components to dissipate heat becomes degraded when a layer of dust collects on the component.

Logistical support, too, becomes a vexing problem for computer-intensive organizations when computing systems are scattered throughout a facility. When machine failures occur, the repair person must go to the machine to diagnose and repair the machine. Oftentimes, this entails multiple visits to the machine's location, particularly when the first examination reveals that replacement parts or a replacement machine are needed. Similarly, software upgrades and other performance checks become quite time-consuming tasks when personnel must travel to each machine where the software resides locally.

Finally, many office buildings were designed before the advent of the age of the PC. As a single PC can consume over 300 watts of power, a heavily computerized workplace could potentially demand power in excess of the amount available. Similarly, the heat generated by the large number of computers installed in modern workplaces can easily overwhelm the air conditioning capacity of a building's HVAC system, thereby causing room temperatures to rise above those levels preferred by the occupants of the building.

These concerns have been driving the development of the network computer (or "NC") and other so-called "thin" computer solutions. While various NC designs have been proposed, most entail removal of the auxiliary memory (also known as the hard drive) and substantially reducing the size of the processor. All software applications and data files would be stored on the network and the NC would be limited to accesses of network software and data files. Most NC designs also propose that all disk drives

(typically, the CD and floppy drives) be removed, thereby eliminating the ability of the NC user to import or export software applications and/or data files.

5 The development of the NC is, in part due to a recognition by the computer industry of security and other problems which have arisen due to the evolution of computer networks into their present configuration. However, the NC is not a fully satisfactory solution to these problems. While removing much of the processing capability from the workstation, most NC designs propose leaving sufficient intelligence at the workstation
10 to access the internet, load software applications retrieved from the network memory and perform other operations. Thus, while reduced in complexity, NCs will still have maintenance, power and cooling concerns. Thus, while the NC represents a step in the right direction, many of the aforementioned issues cannot be resolved by wide-scale implementation of NCs.

15 In order to fully resolve the aforementioned issues, the entire computing system needs to be physically separated from the human interface, specifically, by keeping the human interface (monitor, keyboard, mouse and printer) at the workstation while relocating the associated computing system (chassis holding the motherboard, power supply, memory,
20 disk drives, etc.) to a secured computer room where plural computing systems are maintained. By securing the computing systems in one room, the employer's control over the computer systems would be greatly enhanced. For example, since employees would no longer have personal access, through the floppy or CD drive, to the memory subsystem,
25 employees could not surreptitiously remove information from their computing system. Nor could the employee independently load software or other data files onto their computing system. Similarly, the employee could no longer physically change settings or otherwise modify the hardware portion of the computer. Maintenance would be greatly facilitated by

placement of all of the computing systems in a common room. For example, the repair technicians and their equipment could be stationed in the same room with all of the computing systems. Thus, a technician could replace failed components or even swap out the entire unit without making repeated trips to the location of the malfunctioning machine. Such a room could be provided with special HVAC and power systems to ensure that the room is kept clean, cool and fully powered.

Therefore, what is needed is a computer network comprised of plural computers, each configured such that a human interface portion thereof is remotely located relative to a computing system portion thereof, in which plural computing systems are located at a common location.

Summary of the Invention

In one embodiment, the present invention is of a computer network comprised of a plurality of interconnected nodes, each having a DTE device coupled thereto. At least one, and preferably, plural ones, of the DTE devices are each further comprised of a computing system positioned at a first location, preferably common to the plural computing systems, and a human interface positioned at a second location remotely located relative to the first location. A 4-wire cable couples first and second interface devices which, in turn, are respectively coupled to the computing system and the human interface. The first interface device converts signals generated by the computing system into a format suitable for transmission to the second interface device while the second interface device converts signals, received from the first interface device into a format suitable for transmission to the human interface. In alternate aspects thereof, the computer network may further include a cable, preferably, a thin wire coaxial cable, for interconnecting the plural nodes, the computing system may be a computer chassis and at least one computing system component housed therein and

coupled to the first interface device and the human interface may be a video monitor, printer, keyboard or mouse coupled to the second interface device.

In another embodiment, the present invention is of a computer network comprised of a plurality of interconnected nodes, each having a DTE device coupled thereto. At least one, and preferably, plural ones, of the DTE devices are each further comprised of a computing system positioned at a first location, preferably common to the plural computing systems, and a human interface, which includes a video monitor and at least one I/O device, positioned at a second location remotely located relative to the first location. The DTE device further includes a first encoder coupled to the computing system, a first decoder coupled to the video monitor and the at least one I/O device and a transmission line which couples the encoder to the decoder. The first encoder receives, from the computing system, a video signal to be transmitted to the video monitor and a non-video signal to be transmitted to the at least one I/O device. The first encoder combines the video and the non-video signals into a combined signal and transmits the combined signal to the first decoder via the transmission line. The first decoder receives the combined signal, separates the video and non-video signals therefrom for respective propagation to the video monitor and the at least one I/O device.

In one aspect thereof, the computer may further include a second encoder coupled to the computing system and the first encoder and a second decoder coupled to the first decoder and the I/O devices. The second encoder receives a first non-video signal to be transmitted to a first I/O device, a second non-video signal to be transmitted to a second I/O device and a third non-video signal to be transmitted to a third I/O device and combines the first, second and third non-video signals into the non-video signal. The second decoder receives the non-video signal from the first decoder and separates the first, second and third non-video signals

therefrom for respective propagation to the first, second and third I/O devices. In a further aspect thereof, the first encoder may receive red ("R"), green ("G"), blue ("B"), horizontal synchronization ("HSYNC") and vertical synchronization ("VSYNC") video signals from the computing system,
5 combine the R and HSYNC video signals into a combined signal for transmission to the first decoder, combine the B and VSYNC video signals into another combined signal for transmission to the first decoder and combine the G video signal and the non-video signal into the last combined signal for transmission to the first decoder.

10 In still another embodiment, the present invention is of a computer network comprised of a plurality of nodes, each having a DTE device coupled thereto, and a connective structure arranged, for example, in a bus topology, which interconnects the plural DTE devices into a computer network. The DTE device coupled to at least one, and preferably, plural
15 ones, of the nodes, further comprises a computing system located at a first location, preferably common to the plural computing systems, for example, a shared computer room or a common support structure such as a rack, a human interface located at a second location, each remotely located relative to the first location and preferably remotely located relative to the other
20 second locations, a first interface device coupled to the computing system, a second interface device coupled to a monitor and an I/O device of the human interface and a 4-wire cable coupling the first and second interface devices. An encoding circuit of the first interface device receives, from the computing system, plural video signals to be transmitted to the video monitor and a
25 non-video output signal to be transmitted to the I/O device. The encoding circuit combines the non-video signal with a selected one of the plural video signals to produce a combined signal and transmits the combined signal over a selected pair of the transmission lines of the 4-wire cable. A decoding circuit of the second interface device receives the combined signal

from the first interface device and separates the combined signal into the video signal to be transmitted to the video monitor and the non-video signal to be transmitted to the I/O device.

In one aspect thereof, an encoding circuit of the second interface device receives a non-video input signal from the I/O device and encodes the received signal for output onto a selected pair of the transmission lines for transfer to the first interface device. In another aspect thereof, a decoding circuit of the first interface device receives the non-video I/O input signal from the selected pair of transmission lines and decodes the non-video input signal for transmission to the computing system.

Brief Description of the Drawings

Fig. 1 is a block diagram of a computer network constructed in accordance with the teachings of the present invention.

Fig. 2 is an expanded block diagram of a DTE forming part of the computer network of Fig. 1;

Fig. 3 is an expanded block diagram of upstream extension and downstream extension interfaces of the DTE of Fig. 2.

Fig. 4 is an expanded block diagram of a data encoder/decoder circuit of the upstream extension interface of Fig. 3.

Fig. 5 is an expanded block diagram of a data decoder/encoder circuit of the downstream extension interface of Fig. 3.

Fig. 6 is a circuit diagram illustrating a video-data encoder/3-to-4 wire converter circuit of the upstream extension interface of Fig. 3.

Fig. 7 is a circuit diagram illustrating a video-data decoder/4-to-3 wire converter of the downstream extension interface of Fig. 3

Detailed Description of the Preferred Embodiment

Referring now to Fig. 1, a computer network 1 constructed in accordance with the teachings of the present invention will now be described in greater detail. The computer network 1 has a bus topology and is comprised of a network cable 2 which extends between terminators 3a and 3b. It should be noted, however, that ring, star, hub and other network topologies are equally suitable for use as the network topology. As illustrated herein, the network cable 2 is comprised of "thin wire" coaxial cable. It should be noted, however, that the transmission medium used for the network cable 2 will vary depending on the specific design of the computer network 1. If the network cable 2 can be kept shorter than 100 meters, it may be possible to use a twisted pair as the network cable 2. For greater distances, the network cable should be comprised of either thin wire or "thick wire" coaxial cable. Thin wire coaxial cable has a diameter of 0.25 inches, half that of thick wire coaxial cable. Thick wire cable also requires the use of additional wiring commonly known as "drop cable" and transmit and receive electronics between each tap on the main coaxial cable and the point of attachment to each workstation. As a result, therefore, thin wire coaxial cable is both easier to use and less expensive to install. However, as thin wire has much higher attenuation rates, it is often necessary to use thick wire cable for certain portions of the network cable 2, for example, when interconnecting thin-wire segments located in different areas of a building.

Spaced along the network cable 2 are a series of nodes 4a through 4i. At each node 4a through 4i, a physical connection couples a corresponding one of the DTE devices 6a through 6i to the network cable 2 such that the DTE devices 6a through 6i may access other portions of the computer network 1. In the embodiment of the invention disclosed herein, it is contemplated that each of the DTE devices 6a through 6c are file servers or

other type of network resources while each of the DTE devices 6d through 6i are PCs, specifically PCs comprised of a computing system 12 coupled to a remotely located human interface 14. As further illustrated in Fig. 1, the computing systems 12 are commonly located. For example, the computing systems 12 may be mounted in a common support structure 5 such as a rack located in a room 7 shown in phantom in Fig. 1. As disclosed herein, the term "commonly located" computing systems shall mean computing systems which are positioned or otherwise located within 10 meters of each other. Furthermore, other types of support structures are equally suitable for the uses contemplated herein.

Various benefits are achieved by configuring the computer network 1 to include plural DTE devices, specifically the DTE devices 6d through 6i, each comprised of a commonly located computing system 12 for which the human interface 14 is remotely located relative to the corresponding computing system 12. Specifically, it is well appreciated in the art that the various DTE devices which comprise a computer network are typically geographically scattered throughout a building or other complex, thereby leading to the maintenance, repair and, if the users of the DTE devices have access to network facilities via a floppy drive or other device, security problems discussed herein. All of these problems may be readily eliminated by housing all of the computing systems 12 in one or more support structures 5 which, in turn, may be located in a secured, limited access computer room 7 specially designed to meet the power and cooling requirements for the collection of commonly located computing systems 12.

The computer network 1, itself, is much more compact (and much less geographically extensive) when the computing systems 12 for the DTE devices 6d through 6i are commonly located. Thus, ease of maintenance for the computer network 1 may be enhanced. Furthermore, the cabling requirements for the computer network 1 may be greatly simplified. For

example, most computer networks will include sections which use the more expensive thick wire coaxial cable. If various ones of the DTE devices have commonly located computing systems, the length of cable needed to wire the computer network 1 will be reduced considerably and larger portions of the computer network 1 will be suited for thin wire coaxial cable. For example, if the DTE devices 6a through 6c (the file server and other network facilities) are housed in the same room as the DTE devices 6d through 6i having the commonly located computing systems 12, a thin wire coaxial cable may be suitable for use as the network cable 2 for the entire computer network 1. If so, the cost of installing the computer network 1 will be reduced substantially.

Referring next to Fig. 2, the computing system 12 and human interface 14 which, in combination, respectively comprise each of the DTE devices 6d through 6i may now be seen in greater detail. As may now be seen, the computing system 12 of each DTE device 6d through 6i is comprised of a computer chassis 12a, sometimes referred to as the "box" in which motherboard 12b, disk drive 12c, hard drive 12d, power supply (not shown) and other conventional components, are housed. As may now be further seen, the human interface 14 of each one of the DTE devices 6d through 6i is comprised of a monitor 16, a keyboard 18, a mouse 20 and a printer 22, all of which are conventional devices, the operation of which are well known. It should be clearly understood that the disclosed human interface 14 is given by way of example. Accordingly, it is fully contemplated that other input/output (or "I/O") devices, for example, a joystick, trackball, touchpad or other device may be included as part of the human interface 14. Generally, for inclusion in the human interface 14, an I/O device should require, at a minimum, some type of physical interaction with a human during the primary operation thereof. It should also be understood that not all I/O devices form part of the human interface. For

example, the primary interaction which occurs during use of a floppy or CD drive is between the computing system and a physical medium inserted into the drive. Accordingly, floppy and CD drives are not part of the human interface 14.

5 In a conventionally configured computer system, the monitor 16, the keyboard 18, the mouse 20 and the printer 22 would be provided with a respective cable which terminates in a pin connectors which, when inserted into a matching plug connector provided on a rear side surface (or “backplane”) of the computing system 12, couples the monitor 16, the
10 keyboard 18, the mouse 20 and the printer 22 to the main system bus (not shown) which couples the various electronic devices (including, but not limited to the motherboard 12b, the disk drive 12c and the hard drive 12d) which comprises the computing system 12. Unlike the conventionally configured computer system, however, the monitor 16, the keyboard 18, the
15 mouse 20 and the printer 22 are remotely located relative to the computing system 12. To enjoy the benefits of a remotely located human interface 14 as described herein, it is generally contemplated that the computing system 12 and the human interface 14 be located in separate rooms, which typically requires a minimum separation of at least 10 feet. It is specifically
20 contemplated, however, that the computing system 12 and the human interface 14 may be located hundreds, or even thousands, of feet apart. Thus, by the term “remotely located”, it is intended to refer to separation distances greater than those possible using conventionally designed cables such as those provided when purchasing a PC. Accordingly, the term
25 “remotely located”, as used herein, generally refers to separation distances between 10 and 1,000 feet. However, as it is possible to utilize the disclosed techniques to separate the computing system 12 and the human interface 14 by distances greater than 1,000 feet, it should be clearly understood that the aforementioned upper limit of 1,000 feet is given by way of example and

should not be construed as a limitation on the scope of the present invention.

To achieve the separation distances contemplated herein, an upstream extension interface 24 is coupled to the computing system 12 and a downstream extension interface 26 is coupled to the human interface 14. Generally, connector cables extending from the monitor 16, the keyboard 18, the mouse 20 and the printer 22 all plug into the downstream extension interface 26 in an manner identical to how those same cables would plug into the backplane of the computing system 12. Similarly, the cables extending from the upstream extension interface 24 identically plug into the backplane of the computing system 12 as would the cables from the monitor 16, the keyboard 18, the mouse 20 and the printer 22 plug therinto. Finally, coupling the upstream extension interface 24 and the downstream extension interface 26 is a 4-wire cable 28 configured in the manner disclosed in co-pending U.S. Patent Application Serial No. 08/674,626 filed July 3, 1996 entitled "Method and Apparatus for Enabling the Transmission of Multiple Wide Band Width Electrical Signals, assigned to the Assignee of the present application and hereby incorporated by reference as if reproduced in its entirety.

Referring next to Fig. 3, the upstream and downstream extension interfaces 24 and 26 will now be described in greater detail. As may now be seen, the upstream extension interface 24 is comprised of a video-data encoder/3-to-4 wire converter circuit 30 and a data encoder/decoder circuit 32. Similarly, the downstream extension interface 26 is comprised of a 4-to-3 wire converter/video-data decoder circuit 34 and a data decoder/encoder circuit 36. Broadly speaking, the video-data encoder/3-to-4 wire converter circuit 30 receives video signals output by the computing system 12 for transmission to the monitor 16, specifically, red (or "R"), green (or "G"), blue (or "B"), horizontal synchronization (or "HSYNC") and vertical

synchronization (or "VSYNC") signals. The data encoder/decoder circuit 32, on the other hand, receives all signals output by the computing system 12 for transmission to the keyboard 18, the mouse 20 and the printer 22. The data encoder/decoder circuit 32 also receives the HSYNC and VSYNC signals from the video-data encoder/3-to-4 wire converter circuit 30 and, as will be more fully described below, uses the HSYNC and VSYNC signals to encode data received from the computing system 12 into a data signal DATA_TX for transmission to the video-data encoder/3-to-4 wire converter circuit 30.

Referring next to Fig. 4, operation of the data encoder/decoder circuit 32 in producing the data signal DATA_TX will now be described in greater detail. As is well known in the art, the computing system 12 generates signals to be transmitted to the various I/O devices included as part of the human interface 14. As shown here, the computing system 12 generates KEYBOARD_OUT, MOUSE_OUT and PRINTER_OUT signals for respective propagation to the keyboard 18, the mouse 20 and the printer 22. Each of the output signals KEYBOARD_OUT, MOUSE_OUT and PRINTER_OUT are propagated to a respective buffer 42, 44 and 46 where the received data is held temporarily. The buffers 42, 44 and 46 each include an output tied to a respective input of 3:1 multiplexer 50, the output of which is the DATA_TX signal. The buffers 42, 44 and 46 and the multiplexer 50 are controlled by a controller 48. Specifically, respective control outputs of the controller 48 are tied to a control input of each of the buffers 42, 44 and 46 and to a control input of the multiplexer 50.

The controller 48 times the propagation of the KEYBOARD_OUT, MOUSE_OUT and PRINTER_OUT signals such that the combined signal DATA_TX contains data only during the horizontal and vertical blanking pulses of the video signal being transmitted to the video-date encoder/3-to-4 wire converter circuit 30. To do so, the controller 48 receives the HSYNC

and VSYNC signals from the video-data encoder/3-to-4-wire converter circuit 30. The controller 48 counts the blanking pulses contained in the HSYNC and VSYNC signals, and, during each such blanking pulse of the HSYNC and VSYNC signals, enables a selected one of the buffers 42, 44 and 46 and enables the multiplexer 50 such that the data stored in the selected buffer 42, 44 or 46 is propagated to the video-data encoder/3-to-4 wire converter circuit 30 as part of the DATA_TX signal. For example, each time the video signal transmitted to the video-data encoder/3-to-4 wire converter circuit 30 completes a line of video data, the HSYNC signal will contain a blanking pulse. The number of lines required to generate an image that fills the screen of the video monitor 16 will vary, depending on the operating mode of the video monitor 16. In accordance with one such operating mode, 640 lines of video data are required to generate a image. Thus, for this operating mode, the HSYNC signal will blank 640 times. Each blanking pulse is assigned to an output signal destined for a particular I/O device. For example, during horizontal blanking pulses 1-25, the controller 48 propagates data received from the KEYBOARD_OUT line to the video-data encoder/3-to-4 wire converter circuit 30 by enabling the buffer 42 and the multiplexer 50. During horizontal blanking pulses 26-50, the controller 48 propagates data received from the MOUSE_OUT line to the video-data encoder/3-to-4 wire converter circuit 30 by enabling the buffer 44 and the multiplexer 50. Finally, during horizontal blanking pulses 51-640, the controller 48 propagates data received from the PRINTER_OUT line to the video-data encoder/3-to-4 wire converter circuit 30 by enabling the buffer 46 and the multiplexer 50.

It has been discovered that all output signals respectively generated by the computing system 12 for the keyboard 18 and the mouse 20 may be readily contained within the time consumed by 25 blanking pulses. Furthermore, the 590 blanking pulses assigned for the transmission of

output signals from the computing system 12 to the printer 22 is more than sufficient for containing all of the output signals generated by the computing system 12 for the printer 22 and that a number of these blanking pulses may be reassigned to support additional I/O devices. Finally, still more I/O devices may be supported by placement of output signals generated by the computing system 12 into the vertical blanking pulses contained in the VSYNC signal which occur each time a screen is scanned.

It is contemplated that the controller 48 performs the disclosed combining of the KEYBOARD_OUT, MOUSE_OUT and PRINTER_OUT signals into a combined output signal encoded such that all of the data occurs during the horizontal and vertical blanking pulses by executing an algorithm, set forth in microcode maintained and executed by the controller 48. It should be noted that some I/O devices may have multiple output lines instead of the single output line illustrated in Fig. 4 for each of the keyboard 18, mouse 20 and printer 22. For such devices, it is contemplated that the data encoder/decoder circuit 32 should be provided with additional circuitry and/or control signals which combines the multiple output lines into a single output signal. For example, the multiple output lines corresponding to a particular I/O device could be propagated to discrete locations within the buffer 42, 44 or 46 assigned to that I/O device. The microcode which enables the data held into the buffer to be propagated along the DATA_TX line could then be modified so that signals from the different output lines corresponding to a single I/O device could be transmitted during different ones of the blanking pulses assigned to that device.

Referring next to Fig. 6, the video-data encoder/3-to-4 wire converter circuit 30 which receives the DATA_TX signal from the data encoder/decoder circuit 32 is comprised of an encoder circuit 38 coupled to a 3-to-4 wire converter circuit 40. Input to the encoder circuit 38 are the R,

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G, B, HSYNC, VSYNC and DATA_TX signals. The encoder circuit 38 is similar in construction to the encoder circuit described and illustrated in U.S. Patent Application Serial No. 08/935,968 filed September 23, 1997 entitled "Video Data Transmission and Display System and Associated Methods for Encoding/Decoding Synchronization Information and Video Data, assigned to the Assignee of the present invention and hereby incorporated by reference as if reproduced in its entirety. Specifically, operational amplifier U1a combines the R and HSYNC signals into a first combined signal R+HSYNC and operational amplifier U1c combines the B and VSYNC signals into a second combined signal B+VSYNC. In Serial No. 08/935,968, the disclosed system was configured such that the G signal passed through the encoder unchanged. Here, however, the encoder circuit 38 is constructed to include operational amplifier U1b which combines the G and DATA_TX signals into a third combined signal G+DATA_TX. As data received from the computing system 12 and encoded by the data encoder/decoder circuit 32 into the DATA_TX signal is timed such that the data coincides with the blanking period for the G signal, the G and DATA_TX signals may be combined using a circuit identical to the circuits used to combine the R and HSYNC signals and to combine the B and VSYNC signals.

The R+HSYNC, B+VSYNC and G+DATA_TX signals output the encoder circuit 38 are transmitted to the 3-to-4 line converter circuit 40 where the three signals are placed on lines 1-4 of the 4-wire cable 28 for balanced-mode transmission to the human interface 14. The 3-to-4 wire converter 40 operates as described in co-pending U.S. Patent Application Serial No. 08/674,626 filed July 3, 1996 and previously incorporated by reference. Specifically, (R+HSYNC)+ and (G+DATA_TX)+ are placed on line 1, (R+HSYNC)- and (G+DATA_TX)+ are placed on line 2, (B+VSYNC)+ and (G+DATA_TX)- are placed on line 3 and (B+VSYNC)- and (G+DATA_TX)-

are placed on line 4 of the 4-wire cable 28 for balanced mode transmission to the 4-to-3 wire converter/video-data decoder circuit 34.

Referring next to Fig. 7, the 4-to-3 wire converter/video-data decoder circuit 34 which receives the aforementioned video signals from the video-data encoder/3-to-4 wire converter circuit 30 along transmission lines 1-4 is comprised of a 4-to-3 converter 52 coupled to a decoder circuit 54. Input to the 4-to-3 converter 52 are the video signals transmitted along lines 1-4. In the manner more fully described in co-pending U.S. Patent Application Serial No. 08/674,626 filed July 3, 1996 and previously incorporated by reference, the output of operational amplifier U1a is the R+HSYNC signal, the output of operational amplifier U1b is the G+DATA_TX signal and the output of operational amplifier U1c is the B+VSYNC signal. The R+HSYNC, G+DATA and B+VSYNC signals are propagated from the 4-to-3 converter 52 to the decoder circuit 54. There, in the manner more fully described co-pending U.S. Patent Application Serial No. 08/935,968 filed September 23, 1997 and previously incorporated by reference, the output of operational amplifier U3a is the R signal, the output of the operational amplifier U2a is the HSYNC signal, the output of the operational amplifier U3c is the B signal and the output of operational amplifier U2c is the VSYNC signal. In Serial No. 08/935,968, the disclosed system was configured such that the G signal passed through the decoder unchanged. Here, however, the decoder circuit 54 is constructed to include operational amplifiers U3b and U2b, the outputs of which are the G and DATA_TX signals, respectively.

It is an important aspect of the invention that the encoded video-data signal may be transmitted over the relatively inexpensive 4-wire transmission line used to connect telephones to a telecommunications network such as the public switched telephone network (or "PSTN"). As a result, therefore, the cost of cabling the DTE devices 6d through 6i such

that the human interfaces 14 are located between 10 and 1,000 feet from the computing systems 12 is significantly reduced, particularly as the separation distance between the two is increased. Furthermore, the connection requirements for the video-data encoder/3-to-4 wire converter circuit 30 and the 4-to-3 wire converter/video-data decoder circuit 34 are significantly simplified. For example, while the input connector 56 which couples the encoder circuit 38 to cables extending from the computing system is a 15 pin video connector, the output connector 58 which couples the 3-to-4 wire converter 40 to the 4-wire cable 28 is a very inexpensive RJ-11 jack best known for its use as a telephone jack. Similarly, the input connector 60 which couples the 4-to-3 wire converter 52 to the 4-wire cable 28 is another very inexpensive RJ-11 jack while the output connector 62 which couples the decoder circuit 54 to the monitor 16 is another 15 pin video connector.

Referring next to Fig. 5, the data decoder/encoder circuit 36 will now be described in greater detail. As may now be seen, the data decoder/encoder circuit 36 includes a controller 62 which receives the HSYNC and VSYNC signals from the 4-to-3 wire converter/video-data decoder circuit 34 and a 1:3 demultiplexer 64 having, as its data input, the DATA_TX line, a control input tied to an control output of the controller 62 and first, second and third data outputs--KEYBOARD_OUT, MOUSE_OUT and PRINTER_OUT--which are tied to the keyboard 18, the mouse 20 and the printer 22, respectively. The controller 62 separates keyboard, mouse and printer data from the combined DATA_TX signal by instructing the demultiplexer 64 as to when the input signal should be propagated as the KEYBOARD_OUT, MOUSE_OUT and PRINTER_OUT signals, respectively. To do so, the controller 62 receives the HSYNC and VSYNC signals from the 4-to-3 wire converter/video-data decoder circuit 34. The controller 62 counts the blanking pulses contained in the HSYNC and VSYNC signals,

and, during each such blanking pulse of the HSYNC and VSYNC signals, instructs the demultiplexer 64 to propagate that portion of the DATA_TX signal received by the demultiplexer 64 during that blanking pulse to be output from the demultiplexer on a selected one of the KEYBOARD_OUT, MOUSE_OUT or PRINTER_OUT lines. For example, during horizontal blanking pulses 1-25, the controller 64 may propagate data received from the DATA_TX line on the KEYBOARD_OUT line. During horizontal blanking pulses 26-50, the controller 64 may propagate data received from the DATA_TX line on the MOUSE_OUT line. Finally, during horizontal blanking pulses 51-640, the controller 64 may propagate data received from the DATA_TX line on the PRINTER_OUT line. As before, it is contemplated that the controller 62 performs the disclosed separation of the KEYBOARD_OUT, MOUSE_OUT and PRINTER_OUT signals from the combined DATA_TX signal by executing an algorithm set forth in microcode maintained and executed by the controller 62.

Heretofore, only the transmission of signals from the computing system 12 to the keyboard 18, the mouse 20 and the printer 22 which collectively are the I/O devices forming part of the human interface 14 as been described. As it is typically preferred that computer systems are configured for bi-directional exchanges between the computing system 12 and I/O devices such as the keyboard, mouse and printer and the I/O devices, it is desired that the DTE 6d disclosed herein enable the transmission of signals from the keyboard 18, the mouse 20 and the printer 22 to the computing system 12. Such a further enablement of the invention shall now be described in greater detail, again by referring to Figs. 3, 4 and 5.

As may now be seen, signals output by the keyboard 18, the mouse 20 and the printer 22 are respectively transmitted along the KEYBOARD_IN, MOUSE_IN and PRINTER_IN lines to a respective buffer 66, 68 and 70.

Each of the buffers 66, 68 and 70 have a control input tied to a respective control output of the controller 62 and an output tied to a corresponding input of 3:1 multiplexer 72. Similarly, the multiplexer 72 has a control input tied to a control output of the controller 62. As the operation of the data decoder/encoder circuit 36 in combining the KEYBOARD_IN, MOUSE_IN and PRINTER_IN signals into a combined data signal DATA_RX is identical to the operation of the data encoder/decoder circuit 32 in combining the KEYBOARD_OUT, MOUSE_OUT and PRINTER_OUT signals into the combined data signal DATA_TX, further description of the data decoder/encoder circuit 36 in generating the return path signal, hereafter referred to as the DATA_RX signal, is not deemed necessary. Similarly, as the data encoder/decoder circuit 32 includes a 1:3 demultiplexer 74 having an input which receives the DATA_RX signal, a control input tied to a control output of the controller 48 and first, second and third outputs on which KEYBOARD_IN, MOUSE_IN and PRINTER_IN signals are transmitted to the computing system and the data encoder/decoder circuit 32 separates the DATA_RX signal into the KEYBOARD_IN, MOUSE_IN and PRINTER_IN signals in a manner identical to the operation of the data decoder/encoder circuit 36 in separating the KEYBOARD_OUT, MOUSE_OUT and PRINTER_OUT signals from the DATA_TX signal, further description of the data encoder/decoder circuit 32 in generating the KEYBOARD_IN, MOUSE_IN, and PRINTER_IN signals is also not deemed necessary.

Rather than directing the DATA_RX signal to the video-data decoder/4-to-3 wire converter, the DATA_RX signal is directed to a transmitter 76 which splits the DATA_RX line into two identical signals and directly injects the signal on each of lines 3 and 4 of the 4-wire cable 28 in differential mode. As the DATA_RX signal can only go high during the horizontal and/or vertical blanking pulses, data may be bi-directionally

transferred between the computing system 12 and the I/O devices (the keyboard 18, the mouse 20 and the printer 22) without interfering with the video signal being transferred from the computing system 12 to the video monitor 16. Furthermore, the microcode contained in the controllers 48 and 62 may be readily modified to enable bi-directional transmissions. For example, other blanking pulses may be assigned to the KEYBOARD_IN, MOUSE_IN and PRINTER_IN signals. Alternately, the blanking pulses may be subdivided into "in" and "out" portions. For example, some of the horizontal blanking pulses 1-25 may be assigned to KEYBOARD_OUT while others of the horizontal blanking pulses 1-25 may be assigned to KEYBOARD_IN. By enabling the controllers 48 and 62 to distinguish between signals going from the I/O devices to the computing system 12 and signals going from the computing system 12 to the I/O devices, the controllers 48 and 62 can respectively instruct the multiplexers 74 and 64 to ignore signals received from the DATA_RX and DATA_TX lines if such data was received at times indicating that the data is intended to travel in the opposite direction.

As previously stated, the transmitter 76 places DATA_RX on both lines 3 and 4 of the 4-wire cable 28. Lines 3 and 4 are further coupled to inputs of receiver 78 which provides, as its output, the signal DATA_RX. By placing DATA_RX on both lines, noise on the lines may be detected as any differential between the signals respectively received on the lines 3 and 4, thereby providing noise immunization, as well as reduced EMI radiation levels, for transmissions along the lines 3 and 4.

Finally, referring again to Fig. 7, the outputs of the operational amplifiers U1a, U1b and U1c have ganged controls which adjust the frequency response of the system. These controls compensate for the DC and high frequency losses in the 4-wire cable 28 used to connect the upstream extension interface 24 with the downstream extension interface

26. In addition, these controls may be used enhance the image to the user's taste by providing a "tone" control for video in which the high frequency video energy may be boosted to restore edges and definition to the display. As this equalization can make edges easier for tired eyes to detect, and thus recognize, it is a user-adjustable control.

Although an illustrative embodiment of the invention has been shown and described, other modifications, changes, and substitutions are intended in the foregoing disclosure. Accordingly, it is appropriate that the appended claims be construed broadly and in a manner consistent with the scope of the invention.

Claims

WHAT IS CLAIMED IS:

- 1 1. A computer network, comprising:
2 a plurality of interconnected nodes, each one of said nodes having a
3 DTE device coupled thereto and wherein said DTE device coupled to a first
4 one of said nodes further comprises:
5 a computing system located at a first location;
6 a human interface located at a second location, said second
7 location remotely located relative to said first location;
8 a first interface device coupled to said computing system;
9 a second interface device coupled to said human interface;
10 a transmission line coupling said first and second interface
11 devices;
12 said first interface device converting signals generated by said
13 computing system into a format suitable for transmission to said second
14 interface device and said second interface device converting signals, received
15 from said first interface device into a format suitable for transmission to
16 said human interface.
- 1 2. A computer network according to claim 1 wherein said
2 transmission line coupling said first and second interface devices is a 4-wire
3 cable.
- 1 3. A computer network according to claim 1 and further
2 comprising a cable for interconnecting said plurality of nodes.
- 1 4. A computer network according to claim 1 wherein said cable is
2 a thin wire coaxial cable.

1 5. A computer network according to claim 1 wherein said
2 computing system further comprises a computer chassis and at least one
3 computing system component housed in said computer chassis and coupled
4 to said first interface device.

1 6. A computer network according to claim 5 wherein said human
2 interface further comprises a monitor coupled to said second interface
3 device.

1 7. A computer network according to claim 6 wherein said human
2 interface further comprises either a printer, a keyboard, or a mouse coupled
3 to said second interface device.

1 8. A computer network, comprising:
2 a plurality of interconnected nodes, each one of said nodes having a
3 DTE device coupled thereto and wherein said DTE device coupled to a first
4 one of said nodes further comprises:
5 a computing system located at a first location;
6 a human interface located at a second location, said second
7 location remotely located relative to said first location, said human interface
8 comprised of a video monitor and at least one input/output ("I/O") device;
9 a first encoder coupled to said computing system;
10 a first decoder coupled to said video monitor and said at least
11 one I/O device;
12 a transmission line coupling said encoder and said decoder;
13 said first encoder receiving, from said computing system, a
14 video signal to be transmitted to said video monitor and a non-video signal
15 to be transmitted to said at least one I/O device and combining said video
16 and said non-video signals into a first combined signal;
17 said first decoder receiving said first combined signal from said
18 first encoder and separating said video and non-video signals therefrom for
19 respective propagation to said video monitor and said at least one I/O
20 device.

1 9. A computer network according to claim 8 wherein said human
2 interface further comprises first, second and third I/O devices and wherein
3 said computer network further comprises:

4 a second encoder, coupled to said computing system and said
5 first encoder, said second encoder receiving a first non-video signal to be
6 transmitted to said first I/O device, a second non-video signal to be
7 transmitted to said second I/O device and a third non-video signal to be
8 transmitted to said third I/O device and combining said first, second and
9 third non-video signals into said non-video signal received by said first
10 encoder; and

11 a second decoder, coupled to said first decoder and said first,
12 second and third I/O devices, said second decoder receiving said non-video
13 signal from said first decoder, separating said first, second and third non-
14 video signals therefrom and transmitting said first, second and third non-
15 video signals to said first, second and third I/O devices, respectively.

1 10. A computer network according to claim 9 wherein said first I/O
2 device is a keyboard, said second I/O device is a mouse and said third I/O
3 device is a printer.

1 11. A computer network according to claim 9 wherein said first
2 encoder receives R, G, B, HSYNC and VSYNC video signals from said
3 computing system and combines said R and HSYNC video signals into a
4 second combined signal to be transmitted to said first decoder, combines
5 said B and VSYNC video signals into a third combined signal to be
6 transmitted to said first decoder and combines said G video signal and said
7 non-video signal into said first combined signal.

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1 12. A computer network according to claim 11 wherein said first
2 encoder transmits said HSYNC and VSYNC video signals to said second
3 encoder, said second encoder using said HSYNC and VSYNC video signals
4 to time combining said first, second and third non-video signals into said
5 non-video signal.

1 13. A computer network according to claim 12 wherein said first
2 decoder transmits said HSYNC and VSYNC video signals to said second
3 decoder, said second decoder using said HSYNC and VSYNC video signals
4 to time separating said non-video signal into said first, second and third
5 non-video signals.

1 14. A computer network, comprising:
2 a plurality of nodes, each one of said nodes having a DTE device
3 coupled thereto; and
4 a connective structure for interconnecting said DTE devices
5 respectively coupled to said plurality of nodes into a computer network;
6 said DTE device coupled to a first one of said nodes further
7 comprising:
8 a computing system located at a first location;
9 a human interface located at a second location, said second
10 location remotely located relative to said first location, said human interface
11 comprised of a video monitor and an input/output ("I/O") device;
12 a first interface device coupled to said computing system, said
13 first interface device including an encoding circuit;
14 a second interface device coupled to said monitor and said I/O
15 device, said second interface device including a decoding circuit; and
16 a 4-wire cable coupling said first and second interface devices,
17 said 4-wire cable being comprised of first, second, third and fourth
18 transmission lines;
19 said encoding circuit receiving, from said computing system,
20 plural video signals to be transmitted to said video monitor and a non-video
21 output signal to be transmitted to said I/O device, combining said non-video
22 signal with a selected one of said plural video signals to produce a combined
23 signal and transmitting said combined signal over a selected pair of said
24 first, second, third and fourth transmission lines of said 4-wire cable;
25 said decoding circuit receiving said combined signal from said
26 first interface device and separating said combined signal into said video
27 signal for transmission to said video monitor and said non-video signal for
28 transmission to said I/O device.

1 15. A computer network according to claim 14 wherein said second
2 interface device further comprises:

3 an encoding circuit having an input coupled to said I/O device and an
4 output coupled to said selected pair of said transmission lines;

5 said encoding circuit encoding a non-video input signal, received from
6 said I/O device, for transmission to said computing system over said
7 selected pair of transmission lines

1 16. A computer network according to claim 15 wherein said first
2 interface device further comprises:

3 a decoding circuit having first and second inputs coupled to respective
4 ones of said selected pair of said transmission lines and an output coupled
5 to said computing system;

6 said decoding circuit decoding said non-video input signal received
7 from said encoding circuit of said second interface device for transmission to
8 said computing system.

1 17. A computer network having commonly located computing
2 systems, comprising:
3 a plurality of nodes, each one of said nodes having a DTE device
4 coupled thereto; and
5 a connective structure for interconnecting said DTE devices
6 respectively located at said plurality of nodes into a computer network;
7 wherein at least two of said nodes are positioned at a common
8 location and wherein said DTE device coupled to each one of said at least
9 two nodes positioned at said common location further comprises:
10 a computing system located at said common location;
11 a human interface located at a second location, said second
12 location remotely located relative to said common location, said human
13 interface comprised of a video monitor and at least one input/output ("I/O")
14 device;
15 a first interface device coupled to said computing system;
16 a second interface device coupled to said video monitor and
17 said at least one I/O device;
18 a transmission line coupling said first interface device and said
19 second interface device;
20 said first interface device converting a video signal to be
21 transmitted to said video monitor and a non-video signal to be transmitted
22 to said at least one I/O device into at least one signal having a format
23 suitable for transmission to said second interface device and said second
24 interface re-converting said at least one signal having said format, received
25 from said first interface device, into said video signal and said non-video
26 signal for respective propagation to said video monitor and said at least one
27 I/O device.

1 18. A computer network according to claim 17 wherein said
2 computing systems respectively coupled to each one of said at least two
3 nodes are housed together in a shared computer room.

1 19. A computer network according to claim 17 wherein said
2 computing systems respectively coupled to each one of said at least two
3 nodes are housed together in a common support structure located in a
4 shared computer room.

1 20. A computer network according to claim 19 wherein said
2 common support structure is a computer rack.

1 21. A computer network according to claim 20 wherein said second
2 location at which said human interface for said computing system coupled
3 to a first one of said at least two nodes is also remotely located relative to
4 said second location at which said human interface for said computing
5 system coupled to a second one of said at least two nodes is located.

1 22. A computer network having commonly located computing
2 systems, comprising:
3 a plurality of nodes, each one of said nodes having a DTE device
4 coupled thereto; and
5 a connective structure for interconnecting said DTE devices
6 respectively located at said plurality of nodes into a computer network;
7 wherein at least two of said nodes are positioned at a common
8 location and wherein said DTE device coupled to each one of said at least
9 two nodes positioned at said common location further comprises:
10 a computing system located at said common location;
11 a human interface located at a second location, said second
12 location remotely located relative to said common location, said human
13 interface comprised of a video monitor and at least one input/output ("I/O")
14 device;
15 a first encoder coupled to said computing system;
16 a first decoder coupled to said video monitor and said at least
17 one I/O device;
18 a transmission line coupling said encoder and said decoder;
19 said first encoder receiving, from said computing system, a
20 video signal to be transmitted to said video monitor and a non-video signal
21 to be transmitted to said at least one I/O device and combining said video
22 and said non-video signals into a first combined signal;
23 said first decoder receiving said first combined signal from said
24 first encoder and separating said video and non-video signals therefrom for
25 respective propagation to said video monitor and said at least one I/O
26 device.

1 23. A computer network according to claim 22 wherein said
2 connective structure is arranged in a bus topology.

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1 24. A computer network according to claim 22 wherein said second
2 location at which said human interface for said computing system coupled
3 to a first one of said at least two nodes is also remotely located relative to
4 said second location at which said human interface for said computing
5 system coupled to a second one of said at least two nodes is located.

1 25. A computer network according to claim 22 wherein said
2 computing systems respectively coupled to each one of said at least two
3 nodes are housed together in a shared computer room.

1 26. A computer network according to claim 22 wherein said
2 computing systems respectively coupled to each one of said at least two
3 nodes are housed together in a common support structure located in a
4 shared computer room.

1 27. A computer network according to claim 26 wherein said
2 common support structure is a computer rack.

1 28. A computer network according to claim 22 wherein said
2 human interface of said DTE device coupled to each one of said at least two
3 nodes further comprises first, second and third I/O devices and wherein said
4 DTE device coupled to each one of said at least two nodes further comprises:

5 a second encoder, coupled to said computing system and said first
6 encoder, said second encoder receiving a first non-video signal to be
7 transmitted to said first I/O device, a second non-video signal to be
8 transmitted to said second I/O device and a third non-video signal to be
9 transmitted to said third I/O device and combining said first, second and
10 third non-video signals into said non-video signal received by said first
11 encoder; and

12 a second decoder, coupled to said first decoder and said first, second
13 and third I/O devices, said second decoder receiving said non-video signal
14 from said first decoder, separating said first, second and third non-video
15 signals therefrom and transmitting said first, second and third non-video
16 signals to said first, second and third I/O devices, respectively.

1 29. A computer network according to claim 28 wherein said first
2 I/O device is a keyboard, said second I/O device is a mouse and said third
3 I/O device is a printer.

1 30. A computer network according to claim 28 wherein said first
2 encoder receives R, G, B, HSYNC and VSYNC video signals from said
3 computing system and combines said R and HSYNC video signals into a
4 second combined signal to be transmitted to said first decoder, combines
5 said B and VSYNC video signals into a third combined signal to be
6 transmitted to said first decoder and combines said G video signal and said
7 non-video signal into said first combined signal.

1 31. A computer network, comprising:
2 a first computing system located at a first location;
3 a first human interface located at a second location, said second
4 location remotely located relative to said first location, said first human
5 interface comprised of a video monitor and an input/output device;
6 a first interface device coupled to said first computing system, said
7 first interface device including an encoding circuit;
8 a second interface device coupled to said monitor and said
9 input/output device of said first human interface, said second interface
10 device including a decoding circuit;
11 a 4-wire cable coupling said first and second interface devices, said 4-
12 wire cable coupling said first and second interface devices being comprised
13 of first, second, third and fourth transmission lines;
14 said encoding circuit of said first interface device receiving, from said
15 first computing system, a first set of plural video signals to be transmitted
16 to said video monitor of said first human interface and a first non-video
17 output signal to be transmitted to said input/output device of said first
18 human interface, combining said first non-video signal with a selected one
19 of said first set of plural video signals to produce a first combined signal
20 and transmitting said first combined signal over a selected pair of said first,
21 second, third and fourth transmission lines of said 4-wire cable coupling
22 said first and second interface devices;
23 said decoding circuit of said second interface device receiving said
24 first combined signal from said first interface device and separating said
25 first combined signal into said selected one of said first set of plural video
26 signals for transmission to said video monitor of said first human interface
27 and said first non-video signal for transmission to said input/output device
28 of said first human interface;

29 a second computing system commonly located at said first location
30 with said first computing system;

31 a second human interface located at a third location, said third
32 location remotely located relative to said first location and to said second
33 location, said second human interface comprised of a video monitor and an
34 input/output device;

35 a third interface device coupled to said second computing system, said
36 third interface device including an encoding circuit;

37 a fourth interface device coupled to said monitor and said
38 input/output device of said second human interface, said fourth interface
39 device including a decoding circuit;

40 a 4-wire cable coupling said third and fourth interface devices, said 4-
41 wire cable coupling said third and fourth interface devices being comprised
42 of first, second, third and fourth transmission lines;

43 said encoding circuit of said third interface device receiving, from said
44 second computing system, a second set of plural video signals to be
45 transmitted to said video monitor of said second human interface and a
46 second non-video output signal to be transmitted to said input/output device
47 of said second human interface, combining said second non-video signal with
48 a selected one of said second set of plural video signals to produce a second
49 combined signal and transmitting said second combined signal over a
50 selected pair of said first, second, third and fourth transmission lines of said
51 4-wire cable coupling said third and fourth interface devices;

52 said decoding circuit of said fourth interface device receiving said
53 second combined signal from said third interface device and separating said
54 second combined signal into said selected one of said second set of video
55 signals for transmission to said video monitor of said second human
56 interface and said second non-video signal for transmission to said
57 input/output device of said second human interface; and

58 a connective structure for interconnecting said first computing system
59 and said second computing system into a computer network.

1 32. A computer network according to claim 31 wherein said second
2 interface device further comprises:

3 an encoding circuit having an input coupled to said I/O device of said
4 first human interface and an output coupled to said selected pair of said
5 transmission lines of said first 4-wire cable;

6 said encoding circuit of said second interface device encoding a first
7 non-video input signal, received from said I/O device of said first human
8 interface, for transmission to said first computing system over said selected
9 pair of transmission lines of said first 4-wire cable;

10 and wherein said fourth interface device further comprises:

11 an encoding circuit having an input coupled to said I/O device of said
12 second human interface and an output coupled to said selected pair of said
13 transmission lines of said second 4-wire cable;

14 said encoding circuit of said fourth interface device encoding a second
15 non-video input signal, received from said I/O device of said second human
16 interface, for transmission to said second computing system over said
17 selected pair of transmission lines of said second 4-wire cable.

1 33. A computer network according to claim 32 wherein said first
2 interface device further comprises:

3 a decoding circuit having first and second inputs coupled to respective
4 ones of said selected pair of said transmission lines of said first 4-wire cable
5 and an output coupled to said first computing system;

6 said decoding circuit of said first interface device decoding said first
7 non-video input signal received from said encoding circuit of said second
8 interface device for transmission to said first computing system;

9 and wherein said third interface device further comprises:

10 a decoding circuit having first and second inputs coupled to respective
11 ones of said selected pair of said transmission lines of said second 4-wire
12 cable and an output coupled to said second computing system;

13 said decoding circuit of said third interface device decoding said
14 second non-video input signal received from said encoding circuit of said
15 fourth interface device for transmission to said second computing system.

**COMPUTER NETWORK
HAVING COMMONLY LOCATED COMPUTING SYSTEMS**

Abstract of the Disclosure

A computer network comprised of a plurality of interconnected nodes, each having a DTE device coupled thereto. Plural ones of the DTE devices are each further comprised of a computing system positioned at a first location and a human interface, which includes a video monitor, and plural I/O devices, specifically, a keyboard, mouse and printer, positioned at a second location remotely located relative to the first location. The first locations at which the computing systems of the DTE devices are positioned are commonly located. The DTE devices further include a first encoder coupled to the computing system, a first decoder coupled to the video monitor and the at least one I/O device and a transmission line which couples the encoder to the decoder. The first encoder receives, from the computing system, a video signal to be transmitted to the video monitor and a non-video signal to be transmitted to the at least one I/O device. The first encoder combines the video and the non-video signals into a combined signal and transmits the combined signal to the first decoder via the transmission line. The first decoder receives the combined signal, separates the video and non-video signals therefrom for respective propagation to the video monitor and the at least one I/O device.

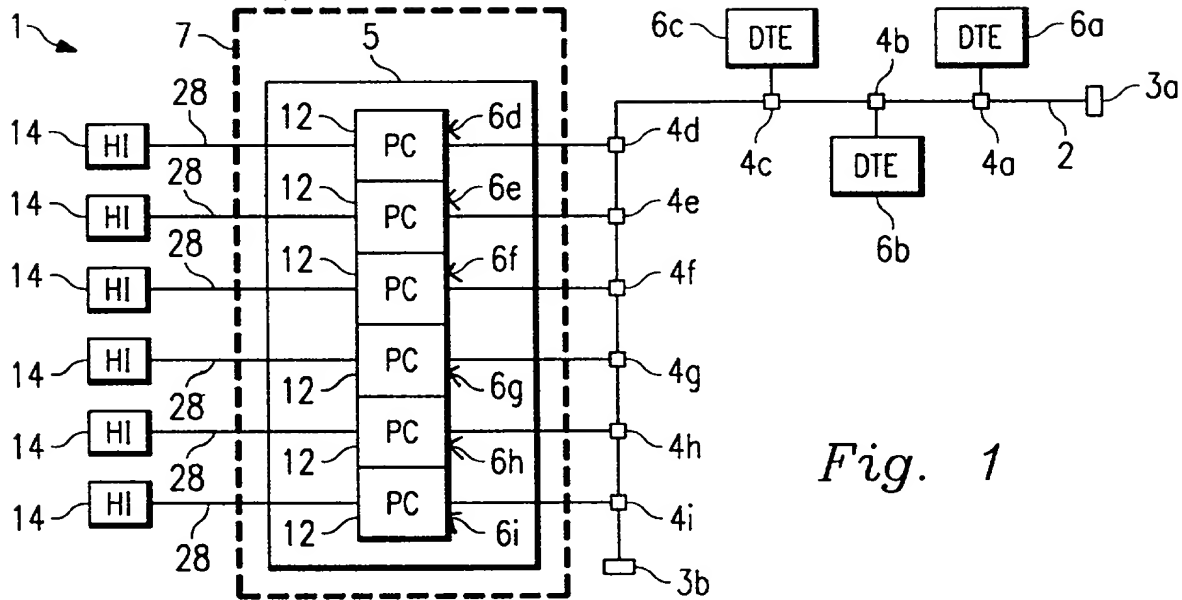


Fig. 1

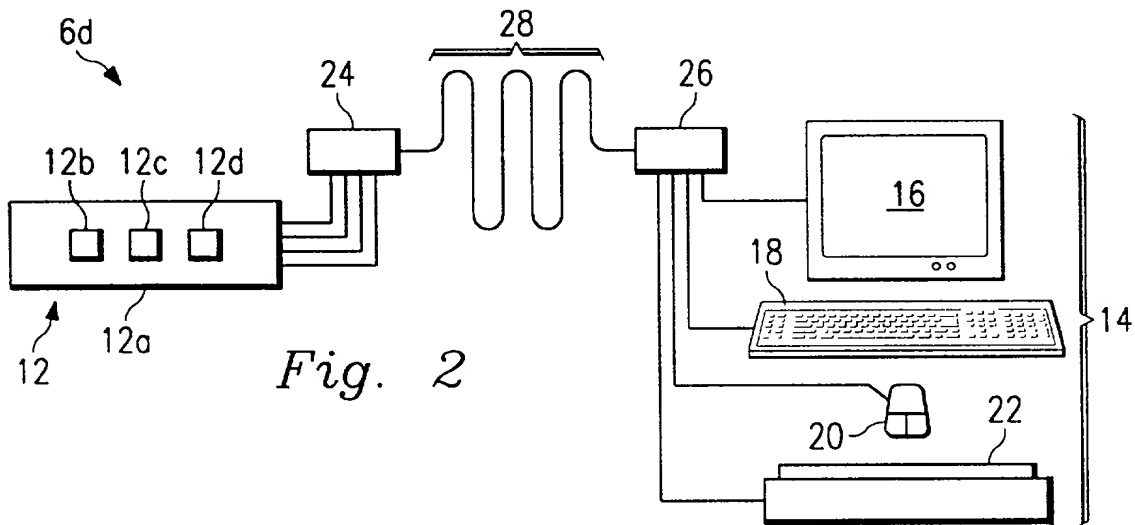


Fig. 2

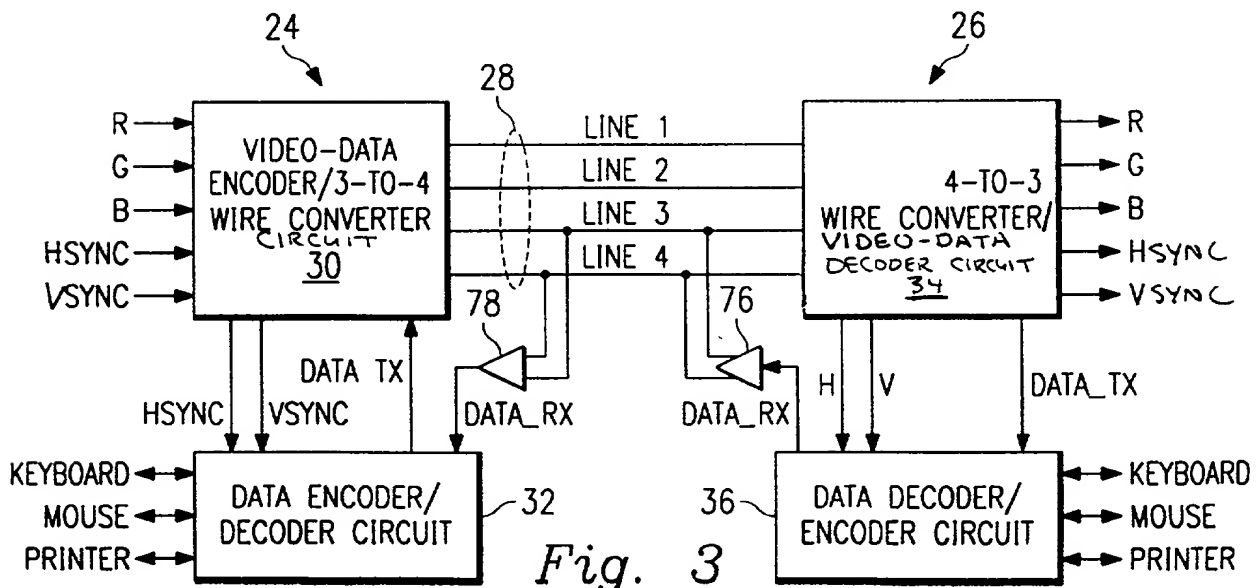
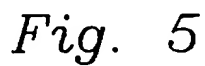
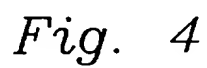


Fig. 3



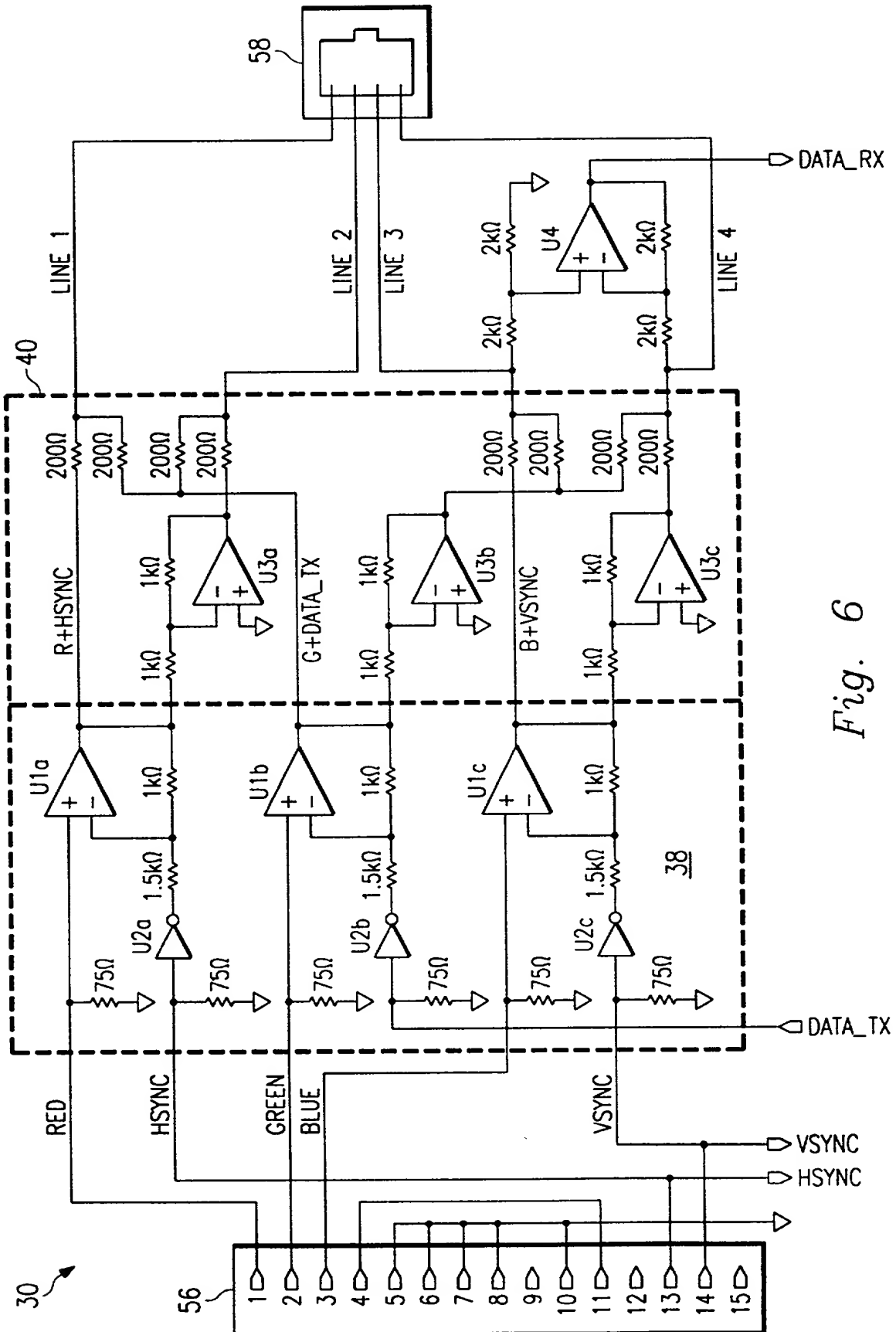
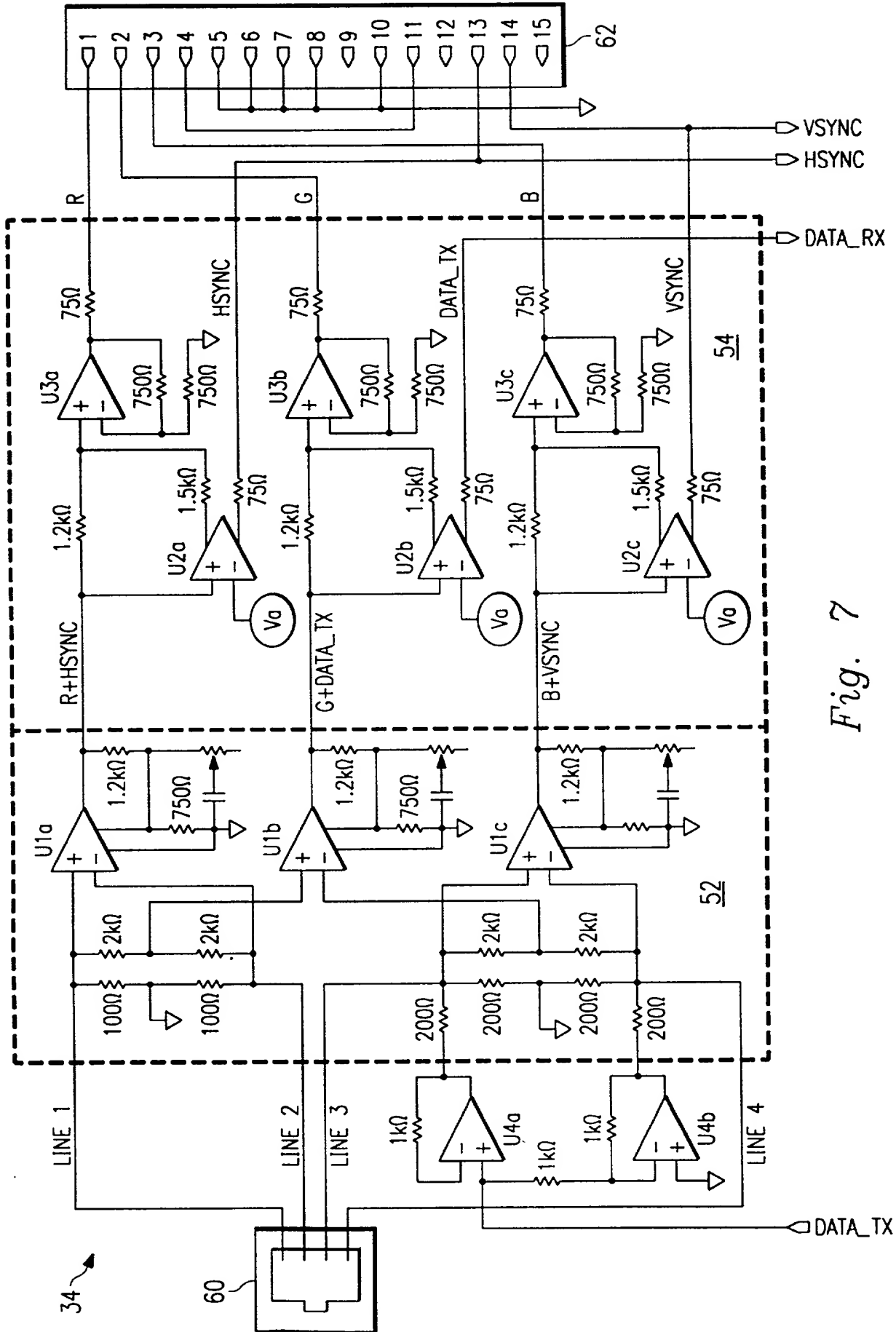


Fig. 6



**DECLARATION AND POWER OF ATTORNEY FOR
PATENT APPLICATION**

As a below named inventor, we hereby declare that:

Our residence, post office address and citizenship are as stated below next to our names;

We believe we are the original, first and joint inventors of the subject matter which is claimed and for which a patent is sought on the invention entitled

COMPUTER NETWORK HAVING COMMONLY LOCATED COMPUTING SYSTEMS

the specification of which: (check one)

 X is attached hereto.

 was filed on _____
under Attorney's Docket Number _____
as Application Serial Number _____

We hereby state that we have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above

We acknowledge the duty to disclose information which is material to the patentability of this application in accordance with 37 CFR 1.56

We hereby declare that all statements made herein of our own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 USC 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

We hereby claim the benefit under Title 35, United States Code, § 119(e) of any United States provisional application(s) listed below.

60/071,604 January 16, 1998
(Application Number) (Filing Date)

POWER OF ATTORNEY: As a named inventor, we hereby appoint the following attorneys and/or agents as our attorneys with full power of substitution and revocation to prosecute this application and to transact all business in the U.S. Patent and Trademark Office connected therewith.

We hereby appoint WARREN B. KICE, Reg. No. 22,732; JEFFREY M. BECKER, Reg. No. 35,442; JAMES R. BELL, Reg. No. 26,528; MICHAEL S. BUSH, Reg. No. 31,745; JUBIN DANA, Reg. No. 41,400; TIM HEADLEY, Reg. No. 31,765; RITA M. IRANI, Reg. No. 31,028; MICHAEL BALCONI-LAMICA, Reg. No. 34,291; DAVID L. McCOMBS, Reg. No. 32,271; DAVID M. O'DELL, Reg. No. 42,044; MICHAEL D. PEGUES, Reg. No. 38,993; PHILLIP B. PHILBIN, Reg. No. 35,979 and BRANDI W. SARFATIS, Reg. No. 37,713 as our attorneys with full power of substitution and revocation to prosecute this application and to transact all business in the U.S. Patent and Trademark Office connected therewith

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INVENTOR'S SIGNATURE: _____ DATE: _____

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FULL NAME OF FOURTH JOINT INVENTOR Charles Ely

INVENTOR'S SIGNATURE: _____ DATE: _____

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CITIZENSHIP: United States of America

POST OFFICE ADDRESS: 504 Rawhide, Horseshoe Bay, Texas 78657